

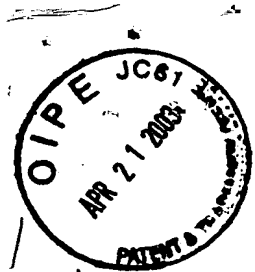
REQUEST FOR DRAWING CHANGE APPROVAL

It is respectfully requested that the Examiner approve the following proposed changes to Figs. 4, 8-10, 15, 17 and 22B of the drawings of the above-identified application:

Please add a dotted outline box within the block designated as "Shift Register", a dotted lead line extending therefrom, and the words "Latch Circuit" at the outer end of the dotted lead line, as shown in red on the attached photocopy of Fig. 4.

Also, please add the legend "PRIOR ART" to Figs. 8-10, 15, 17 and 22B, as shown in red on the attached photocopies of Figs. 8-10, 15, 17 and 22 of the drawings originally filed with this application, respectively.

Approval of the changes to the drawings proposed above in response to this communication will allow the Applicants to file appropriate formal drawings incorporating certain of the changes required by the Examiner upon the allowance of this application. A decision granting approval to the foregoing proposed drawing changes in response to this communication, therefore, is respectfully requested.



Approved
JHW
06/19/03

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Fig.2

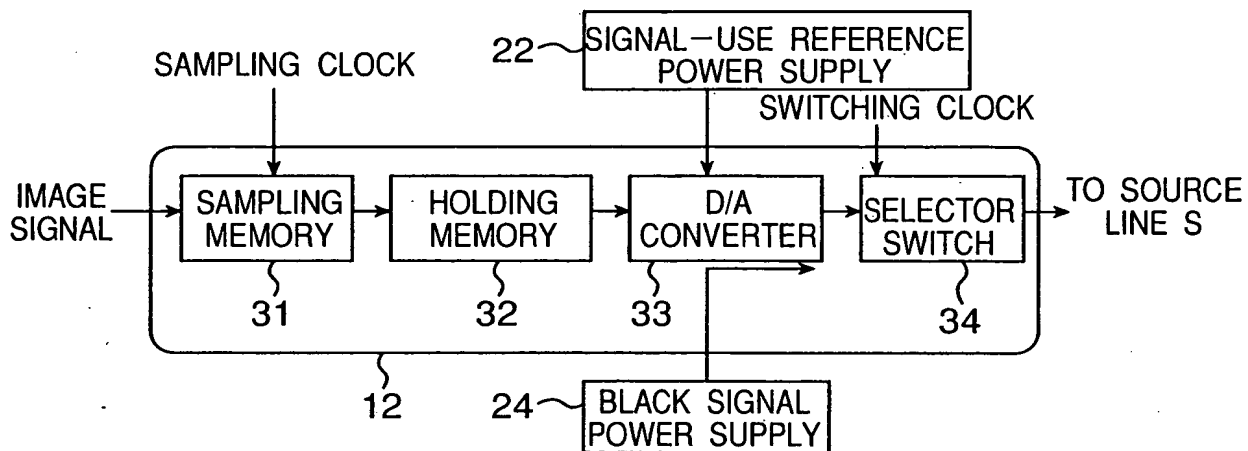


Fig.3

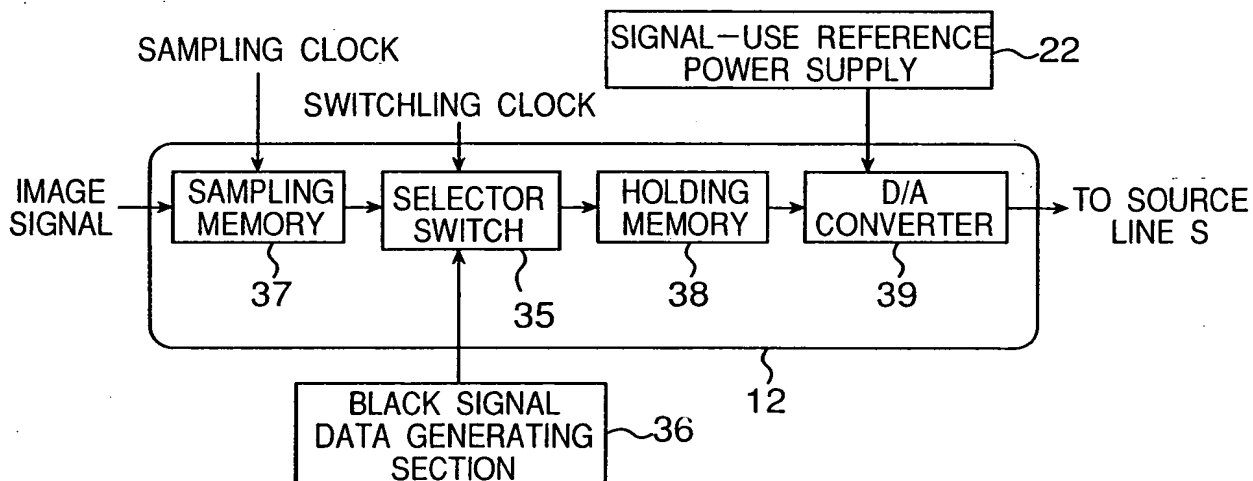


Fig.4

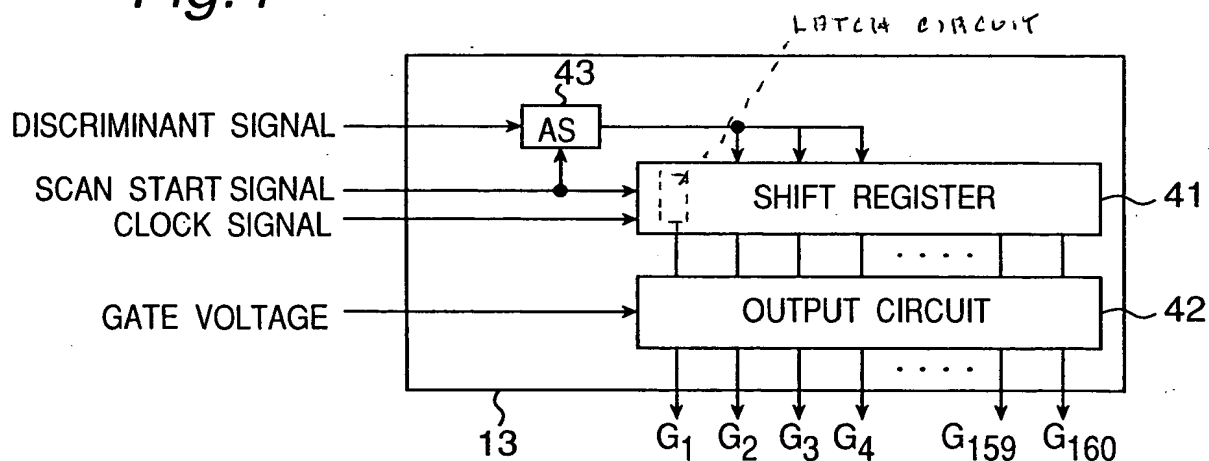




Fig.7

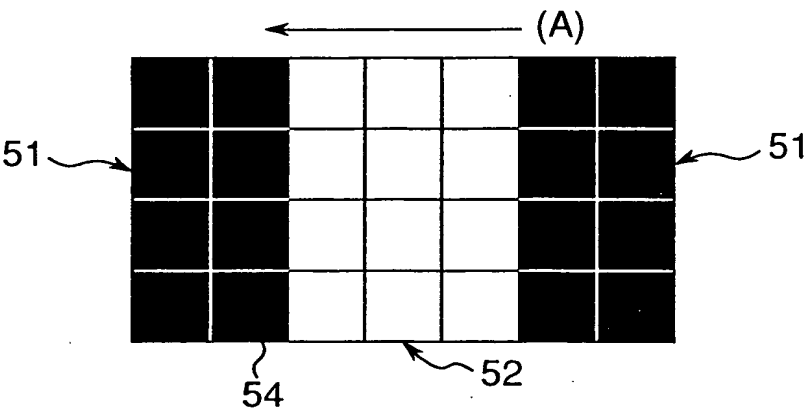


Fig.8 PRIOR ART

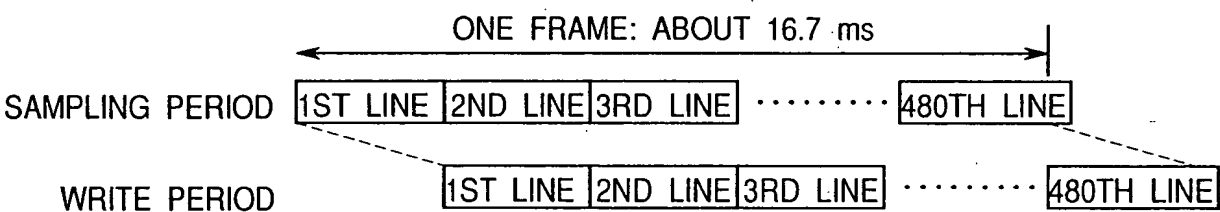


Fig.9 PRIOR ART

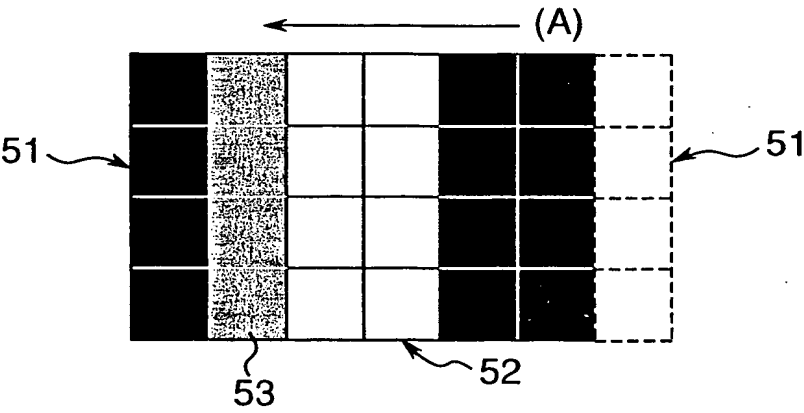




Fig.10 PRIOR ART

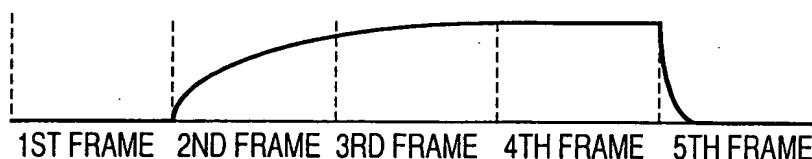


Fig.11A

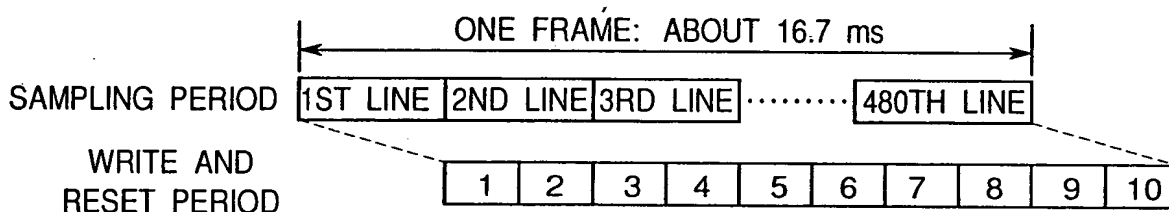


Fig.11B

NO.	STATE OF DRIVE
1	1ST LINE, DATA SIGNAL WRITE
2	161ST LINE, RESET SIGNAL WRITE
3	2ND LINE, DATA SIGNAL WRITE
4	162ND LINE, RESET SIGNAL WRITE
5	3RD LINE, DATA SIGNAL WRITE
6	163RD LINE, RESET SIGNAL WRITE
7	nTH LINE, DATA SIGNAL WRITE
8	(160+n)TH LINE, RESET SIGNAL WRITE
9	480TH LINE, DATA SIGNAL WRITE
10	160TH LINE, RESET SIGNAL WRITE

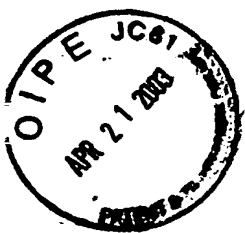


Fig.14

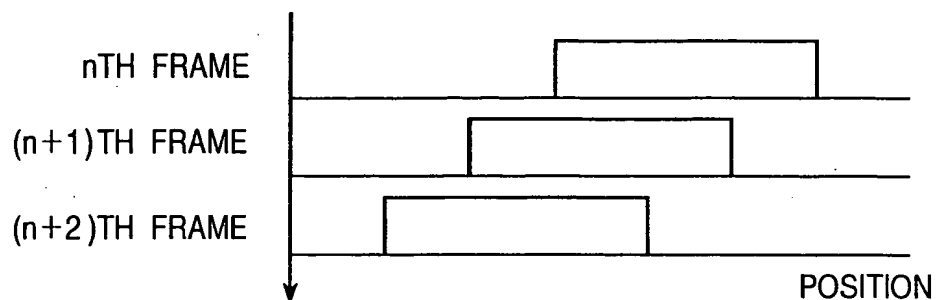


Fig.15

PRIOR RRT

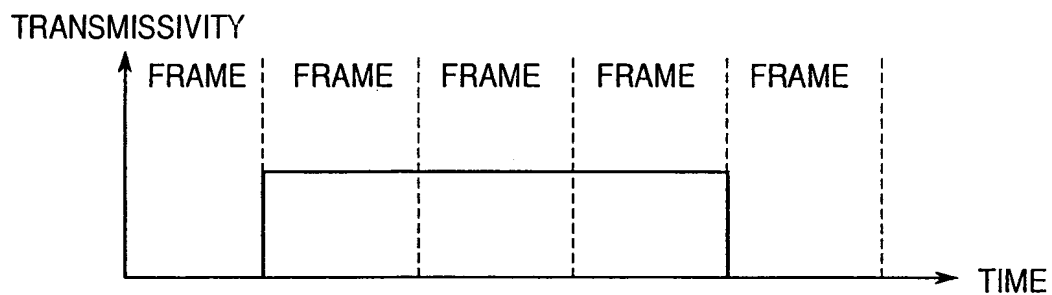


Fig.16

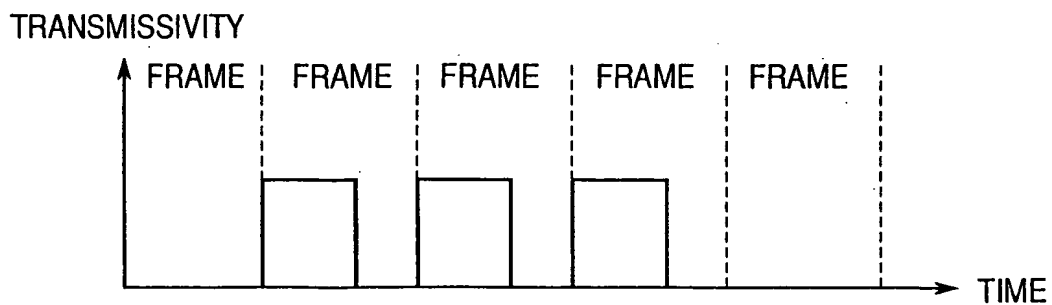




Fig.17

PRIOR ART

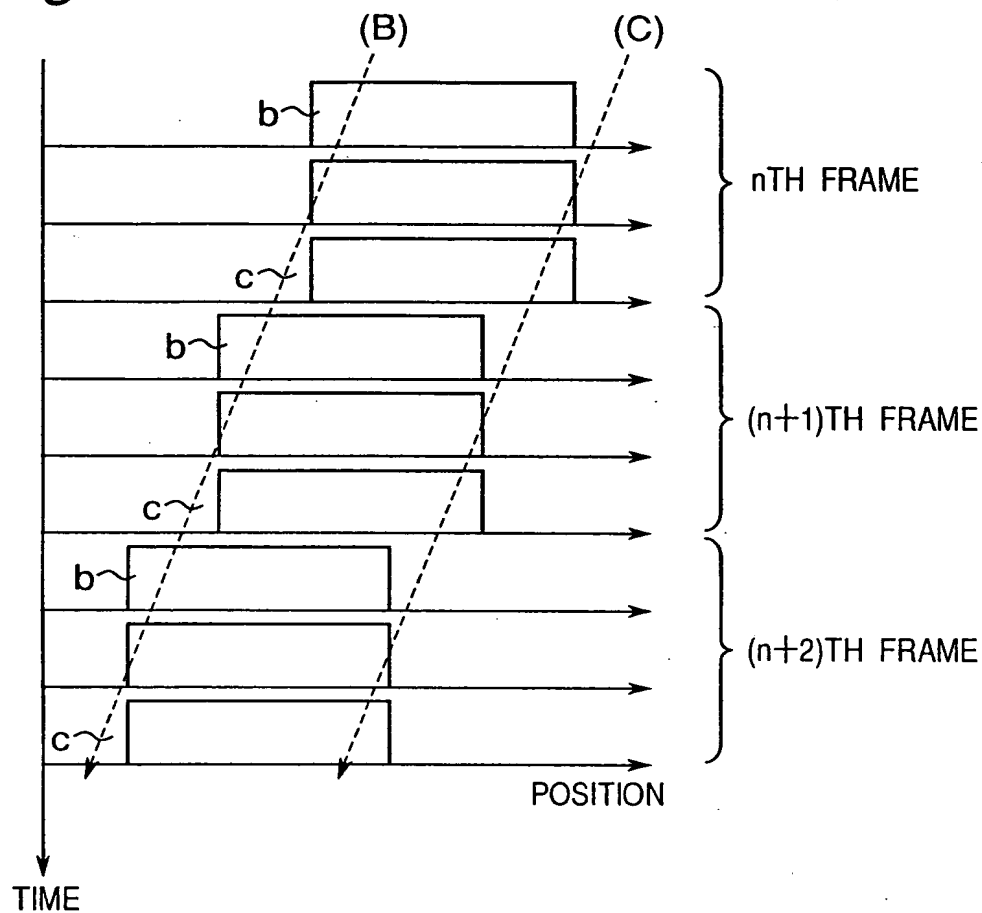
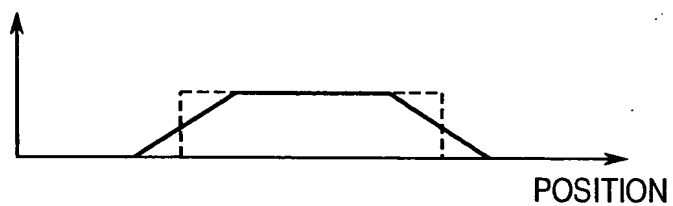


Fig.18

BRIGHTNESS ON RETINA



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Fig.21

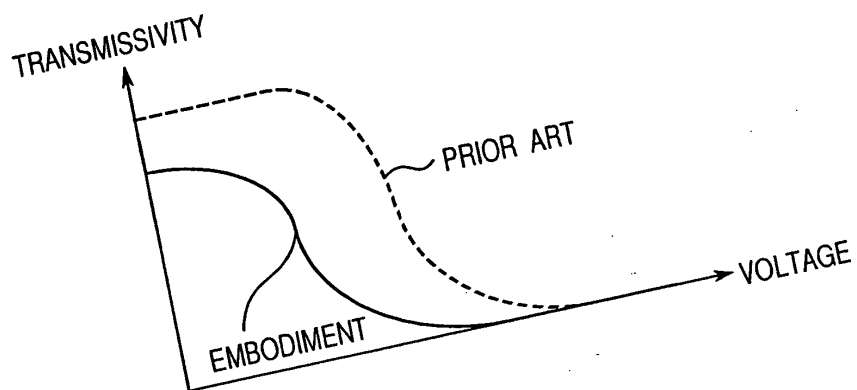


Fig.22A

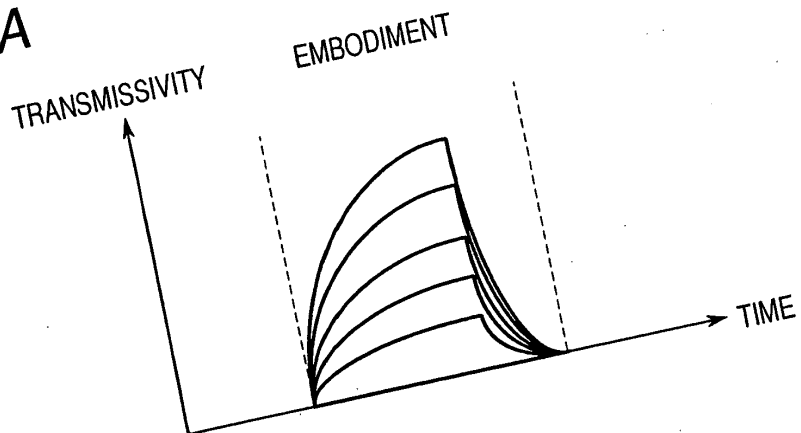


Fig.22B

